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Components, Packaging, and Manufacturing Technology, Part B: Advanced Pa;
Transactions on [see also Components, Hybrids, and Manufacturing Technolo;
Transactions on]Volume 17, Issue 4, Nov. 1994 Page(s):505 - 513
Digital Object Identifier 10.1109/96.338715[AbstractPlus](#) | [Full Text: PDF\(792 KB\)](#) [IEEE JNL](#)
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Pattern Analysis and Machine Intelligence, IEEE Transactions on
Volume 16, Issue 1, Jan. 1994 Page(s):24 - 37
Digital Object Identifier 10.1109/34.273719
[AbstractPlus](#) | [Full Text: PDF\(1312 KB\)](#) [IEEE JNL](#)
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Advanced Packaging, IEEE Transactions on [see also Components, Packaging, Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on]
Volume 25, Issue 2, May 2002 Page(s):143 - 153
Digital Object Identifier 10.1109/TADVP.2002.803270
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(616 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#) 4. **A multiconductor transmission line methodology for global on-chip inter modeling and analysis**Elfadel, I.M.; Deutsch, A.; Smith, H.H.; Rubin, B.J.; Kopcsay, G.V.;
Advanced Packaging, IEEE Transactions on [see also Components, Packaging, Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on]
Volume 27, Issue 1, Feb. 2004 Page(s):71 - 78
Digital Object Identifier 10.1109/TADVP.2004.825478
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 I. M. Elfadel, David D. Ling
 June 1997 **Proceedings of the 34th annual conference on Design automation DAC '97**

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CAD tools and research in the area of reduced-order modeling of large linear interconnect networks have evolved from merely finding a Padé approximation for the given network transfer function to finding an approximate transfer function that preserves such circuit-theoretic properties of the network as stability, passivity, and RLC synthesizability. In particular, preserving passivity guarantees that the reduced-order models will be well-behaved when embedded back in the circuit where the interconnect ...

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 June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

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3 [A block rational Arnoldi algorithm for multipoint passive model-order reduction of multiport RLC networks](#) 
 I. M. Elfadel, David D. Ling

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design****Publisher:** IEEE Computer SocietyFull text available:  [pdf\(225.42 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
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Recent work in the area of model-order reduction for RLC interconnect networks has been focused on building reduced-order models that preserve the circuit-theoretic properties of the network, such as stability, passivity, and synthesizability. Passivity is the one circuit-theoretic property that is vital for the successful simulation of a large circuit netlist containing reduced-order models of its interconnect networks. Non-passive reduced-order

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